

PRODUCT SPECIFICATION

PRODUCT: CERAMIC DISC CAPACITOR
SAFETY RECOGNIZED

TYPE: AS SERIES

CUSTOMER: _____

DOC. NO.: POE-D18-00-E-09

APPROVED BY CUSTOMER

PSA
PASSIVE SYSTEM ALLIANCE

VENDOR :

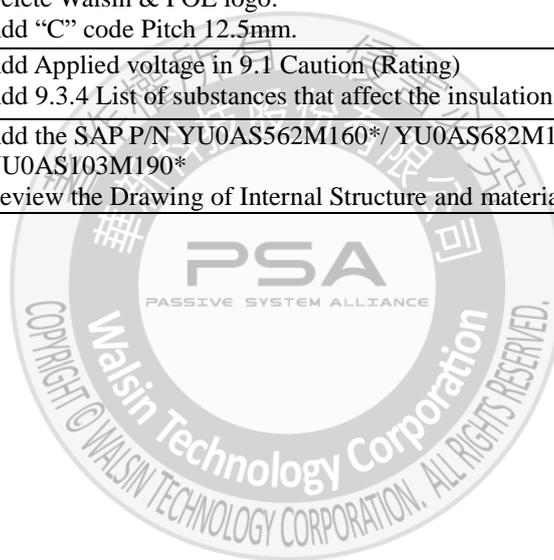
- WALSIN TECHNOLOGY CORPORATION**
566-1, KAO SHI ROAD, YANG-MEI
TAO-YUAN, TAIWAN
- PAN OVERSEAS (GUANGZHOU) ELECTRONIC CO.,LTD.**
NO.277,HONG MING ROAD,EASTERN SECTION,
HUANGPU DISTRICT ,GUANG ZHOU,CHINA
- DONGGUAN WALSIN TECHNOLOGY ELECTRONICS CO., LTD.**
NO.638, MEI JING WEST ROAD,XINIUPO,ADMINISTRATIVE ZONE,DALANGTOWN,DONGGUAN
CITY, GUANGDONG PROVINCE

MANUFACTURE SITE :

- PAN OVERSEAS (GUANGZHOU) ELECTRONIC CO.,LTD.**
NO.277,HONG MING ROAD,EASTERN SECTION,
HUANGPU DISTRICT ,GUANG ZHOU,CHINA

Record of change

Date	Version	Description	page
2014/11/19	0	First edition.	all
2016/1/27	1	1. Review the Available lead code of Lead Configuration. 2. Revised standard NO. of VDE.	5 9
2019/1/18	2	1. Revised recognized NO. of CQC and ENEC (DEMKO).	9
2019/4/1	3	1. Add CQC approval marking on the body.	8
2019/4/24	4	1. "Protrusion length": "2.0max (Or the end of lead wire may be inside the tape.)" revised to "+0.5to-1.0 (Or the end of lead wire may be inside the tape.)" 2. Add "Soldering Recommendation"	7 18
2019/12/11	5	1. Review the Available lead code of Lead Configuration 2. Add "8.3 Label samples "	5 14
2020/4/22	6	1. Review the bulk packing quantity of the code of 14th to15th ≥ 12 .	14
2021/9/9	7	1. Delete Walsin & POE logo. 2. Add "C" code Pitch 12.5mm.	1 4,5,7,14
2022/4/18	8	1. Add Applied voltage in 9.1 Caution (Rating) 2. Add 9.3.4 List of substances that affect the insulation strength of coating	16 18
2025/7/6	9	1. Add the SAP P/N YU0AS562M160*/ YU0AS682M170*/ YU0AS822M190*/ YU0AS103M190* 2. Review the Drawing of Internal Structure and material list	6 21



SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES	POE-D18-00-E-09	Ver: 9	Page: 4 / 21
--	-----------------	--------	--------------

1. Part number for SAP system:

(Ex.) YU 0AS 472 M 14 0 L 20 C 0 H
 (1) (2) (3) (4) (5) (6) (7) (8) (9) (10) (11)

(1)Temperature characteristic (identified code)

CODE	Temperature characteristic	Cap. Change
YP	Y5P	±10%
YU	Y5U	-55% to +20%

(2)TYPE (identified by 3-figure code) : 0AS = AS Type(X1:760V~/Y1:500V~),

(3)Capacitance (identified by 3-figure code):EX.472=4700pF

(4)Capacitance tolerance (identified by code): K:±10%,M:±20%

(5)Nominal body diameter dimension (identified by 2-figure code): 07--Dmax8.0mm, 08--Dmax9.0mm...

(6)Internal code: 0--Normal, other code--Special control

(7)Lead Style : Refer to “2. Mechanical”.

(8)Packing mode and lead length (identified by 2-figure code)

Taping Code	Description
AM	Ammo box and product pitch : 25.4 mm
AS	Ammo box and product pitch : 15.0 mm (Only for the SAP part number 11-12 digits ≤ 10)

Bulk Code	Description
03	Lead length : 3.0mm
3C	Lead length : 3.5mm
3E	Lead length : 3.5mm
04	Lead length : 4.0mm
4E	Lead length : 4.5mm
20	Lead length : 20mm

(9)Length tolerance

Code	Description
A	±0.5 mm (only for kink lead type) Short lead
B	±1.0 mm Short lead
C	Min. Long lead
D	Taping special purpose Taping

(10)Pitch

Code	Description
0	10±1 mm
A	10±0.5 mm
C	12.5± 0.8 mm

(11)Epoxy Resin Code

Code	Description
H	Halogen and Pb free, epoxy resin.

SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES	POE-D18-00-E-09	Ver: 9	Page: 5 / 21
--	-----------------	--------	--------------

2. Mechanical:

Encapsulation : Epoxy resin, flammability UL94 V-0

Available lead code (unit: mm):

Lead type	SAP P/N (13-17)digits	Pitch (F)	Lead Length (L)	Packing	Lead Configuration	
Lead style : L or B Type L or B Straight lead	L03B0	10 ± 1.0	3.0 ± 1.0	Bulk		
	L3CA0	10 ± 1.0	3.3 ± 0.5			
	L4EB0	10 ± 1.0	4.5 ± 1.0			
	L05B0	10 ± 1.0	5.0 ± 1.0			
	L03BC	12.5 ± 0.8	3.0 ± 1.0			
	L3EAC	12.5 ± 0.8	3.5 ± 0.5			
	L4EBC	12.5 ± 0.8	4.5 ± 1.0			
	L20C0	10 ± 1.0	20 min.			
	L20CC	12.5 ± 0.8	20 min.			
	BASD0	10 ± 1.0	Refer to "4. Taping format"			Tap. Ammo
	BAMD0	10 ± 1.0				
BAMDC	12.5 ± 0.8					
Lead style : G Type G Straight lead	G03B0	10 ± 1.0	3.0 ± 1.0	Bulk		
	G4EB0	10 ± 1.0	4.5 ± 1.0			
	G05B0	10 ± 1.0	5.0 ± 1.0			
	GASD0	10 ± 1.0	Refer to "4. Taping format"			Tap. Ammo
	GAMD0	10 ± 1.0				
Lead style : D Type D Vertical kink lead	D03A0	10 ± 1.0	3.0 ± 0.5	Bulk		
	D3EA0	10 ± 1.0	3.5 ± 0.5			
	D04A0	10 ± 1.0	4.0 ± 0.5			
	D03AC	12.5 ± 0.8	3.0 ± 0.5			
	D3EAC	12.5 ± 0.8	3.5 ± 0.5			
	D04AC	12.5 ± 0.8	4.0 ± 0.5			
	D20C0	10 ± 1.0	20 min.			
	DASD0	10 ± 1.0	Refer to "4. Taping format"			Tap. Ammo
	DAMD0	10 ± 1.0				
DAMDC	12.5 ± 0.8					
Lead style : X Type X Outside kink lead	X03A0	10 ± 1.0	3.0 ± 0.5	Bulk		
	X3EA0	10 ± 1.0	3.5 ± 0.5			
	X04A0	10 ± 1.0	4.0 ± 0.5			
	X03AC	12.5 ± 0.8	3.0 ± 0.5			
	X3EAC	12.5 ± 0.8	3.5 ± 0.5			
	X04AC	12.5 ± 0.8	4.0 ± 0.5			
	XAMD0	10 ± 1.0	Refer to "4. Taping format"			Tap. Ammo

* Lead diameter Φd: 0.55 +/-0.05mm

*e (Coating extension on leads): 3.0mmMax for straight lead style, not exceed the kink for kink lead.

SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES	POE-D18-00-E-09	Ver: 9	Page: 6 / 21
--	-----------------	--------	--------------

3. Part numbering/T.C/Capacitance/ Tolerance/Diameter :

SAP P/N	T.C.	Capacitance(pF)	Tolerance	Dimension (unit:mm)				
				D(max.)	T(max.)	F		Φd
						Bulk	Tap.	
YP0AS101K070*	Y5P	100 pF	±10%	8.0	5.5	10.0 Or 12.5	10.0 Or 12.5	0.55+/-0.05
YP0AS151K070*		150 pF		8.0				
YP0AS221K070*		220 pF		8.0				
YP0AS331K070*		330 pF		8.0				
YP0AS471K080*		470 pF		9.0				
YP0AS561K090*		560 pF		10.0				
YP0AS681K090*		680 pF		10.0				
YP0AS102K110*		1000 pF		12.0				
YU0AS102M080*		Y5U		1000 pF				
YU0AS152M090*	1500 pF		10.0					
YU0AS222M120*	2200 pF		13.0					
YU0AS332M120*	3300 pF		13.0					
YU0AS392M130*	3900 pF		14.0					
YU0AS472M140*	4700 pF		15.0					
YU0AS562M160*	5600 pF		17.0					
YU0AS682M170*	6800 pF		18.0					
YU0AS822M190*	8200 pF		20.0					
YU0AS103M190*	10000 pF		20.0					

- The minimum thickness of coating (reinforced insulation) is 0.4mm.

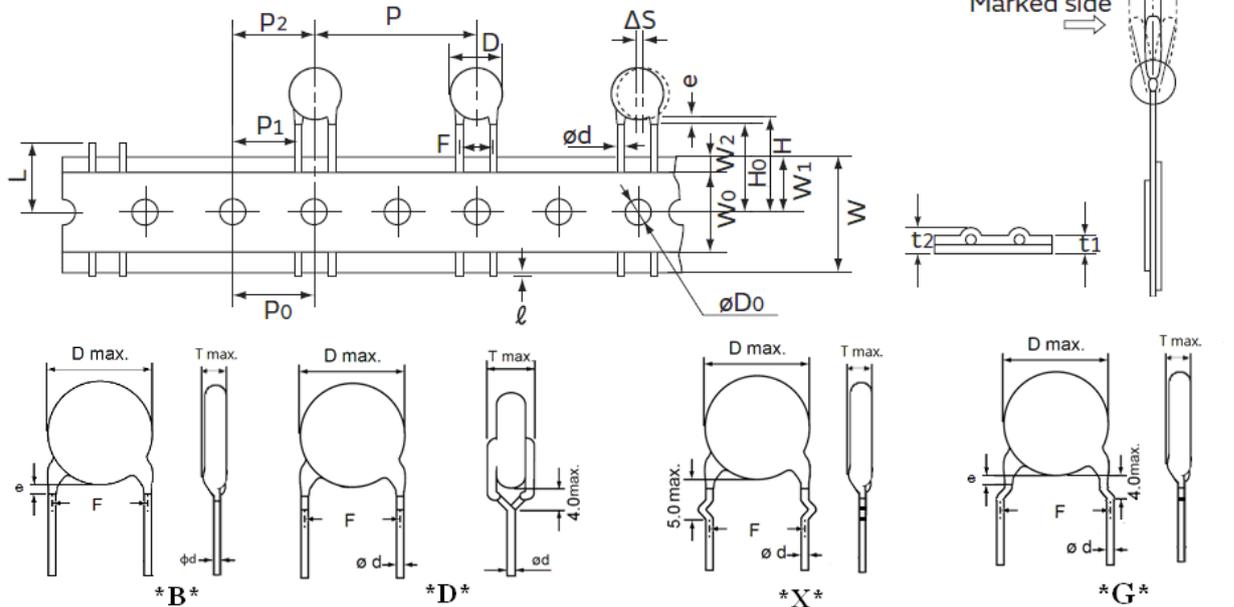


4. Taping format:

- 25.4mm pitch/lead spacing 10.0mm & 12.5mm taping

Lead Code: *BAMD0 & *DAMD0 & *XAMD0 & *GAMD0

*BAMDC & *DAMDC



POE Part Number		*BAMD0 / *DAMD0 *XAMD0 / *GAMD0	*BAMDC / *DAMDC
Item	Symbol	Dimensions(mm)	Dimensions(mm)
Pitch of component	P	25.4 ± 2	25.4 ± 2
Pitch of sprocket	P0	12.7 ± 0.3	12.7 ± 0.3
Lead spacing	F	10.0 ± 1.0	12.5 ± 0.8
Length from hole center to component center	P2	12.7 ± 1.5	12.7 ± 1.5
Length from hole center to lead	P1	7.7 ± 1.5	6.2 ± 1.5
Body diameter	D	See the "3. Part numbering/T.C/Capacitance/ Tolerance/Diameter"	
Deviation along tape, left or right	Δ S	0 ± 2.0	
Carrier tape width	W	18.0 +1/- 0.5	
Position of sprocket hole	W1	9.0 ± 0.5	
Lead distance between the kink and center of sprocket hole	H0	18.0 +2.0/-0 (For: *DAMD0 & *XAMD0&*GAMD0)	18.0 +2.0/-0 (For: *DAMDC)
Lead distance between the bottom of body and the center of sprocket hole	H	20.0+1.5/-1.0 (For: *BAMD0)	20.0+1.5/-1.0 (For: *BAMDC)
Length from the terminal of the lead wire to the edge of carrier tape	ℓ	+0.5 to -1.0 (Or the end of lead wire may be inside the hole-down tape.)	
Diameter of sprocket hole	D0	4.0 ± 0.2	
Lead diameter	φd	0.55 ± 0.05	
Total tape thickness	t1	0.6 ± 0.3	
Total thickness, tape and lead wire	t2	1.5 max.	
Deviation across tape	Δ h1/Δ h2	2.0 max.	
Portion to cut in case of defect	L	11.0 max.	
Hole-down tape width	W0	8.0 min	
Hole-down tape distortion	W2	1.5 ± 1.5	
Coating extension on leads	e	3.0 max for straight lead style; Not exceed the kink leads for kink lead.	
Body thickness	T	See the "3. Part numbering/T.C/Capacitance/ Tolerance/Diameter"	

**SAFETY STANDARDS REGULATED, REINFORCED
INSULATION TYPE, AS SERIES**

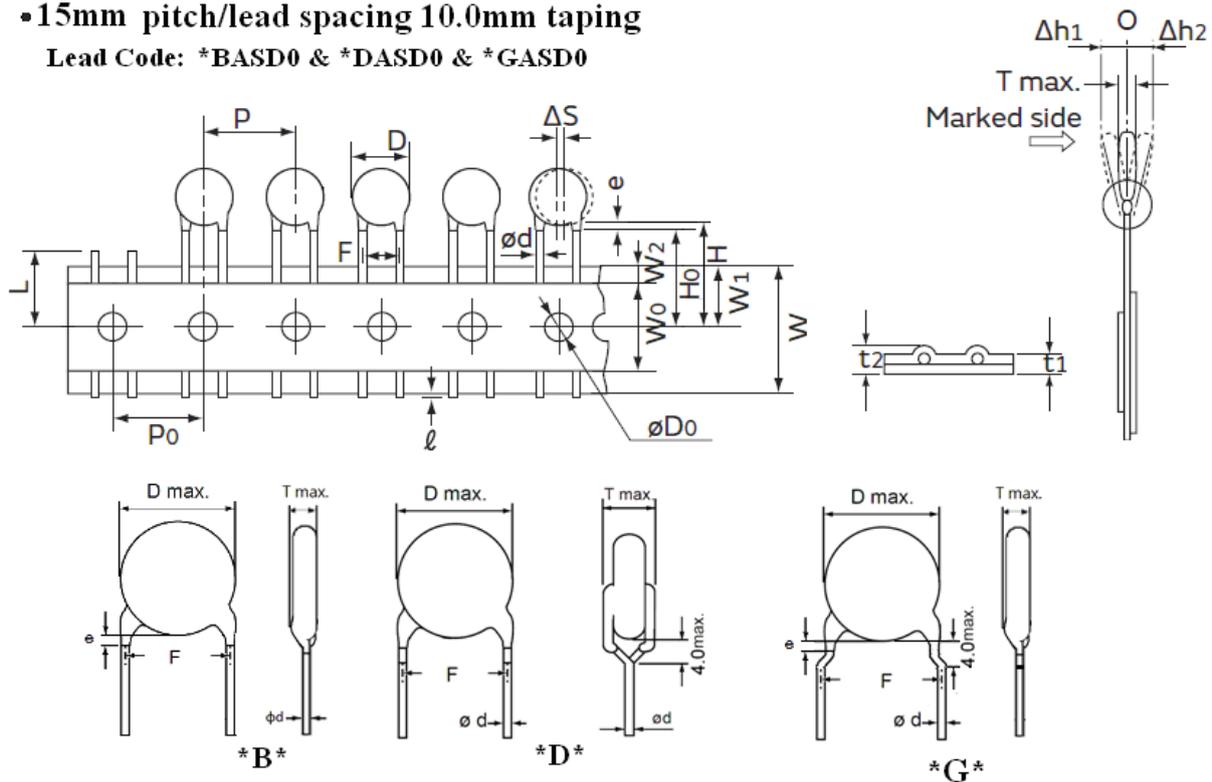
POE-D18-00-E-09

Ver: 9

Page: 8 / 21

• 15mm pitch/lead spacing 10.0mm taping

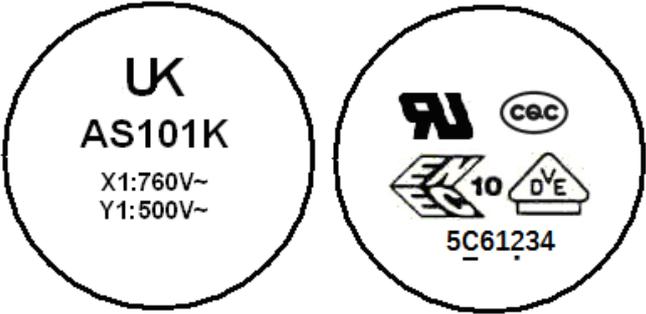
Lead Code: *BASD0 & *DASD0 & *GASD0



POE Part Number		*BASD0/*DASD0/*GASD0
Item	Symbol	Dimensions(mm)
Pitch of component	P	15.0±1
Pitch of sprocket	P0	15.0±0.3
Lead spacing	F	10.0±1.0
Body diameter	D	See the "3. Part numbering/T.C/Capacitance/ Tolerance/Diameter"
Deviation along tape, left or right	Δ S	0 ± 2.0
Carrier tape width	W	18.0 +1/ -0.5
Position of sprocket hole	W1	9.0 ± 0.5
Lead distance between the kink and center of sprocket hole	H0	18.0 +2.0/-0 (For: *DASD0 & *GASD0)
Lead distance between the bottom of body and the center of sprocket hole	H	20.0+1.5/-1.0 (For: *BASD0)
Length from the terminal of the lead wire to the edge of carrier tape	ℓ	+0.5 to -1.0 (or the end of lead wire may be inside the hole-down tape.)
Diameter of sprocket hole	D0	4.0 ± 0.2
Lead diameter	φd	0.55+0.1/-0.05
Total tape thickness	t1	0.6 ± 0.3
Total thickness, tape and lead wire	t2	1.5 max.
Deviation across tape	Δ h1/Δ h2	2.0 max.
Portion to cut in case of defect	L	11.0 max.
Hole-down tape width	W0	8.0 min
Hole-down tape distortion	W2	1.5 ± 1.5
Coating extension on leads	e	3.0 max for straight lead style; Not exceed the kink leads for kink lead.
Body thickness	T	See the "3. Part numbering/T.C/Capacitance/ Tolerance/Diameter"

SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES	POE-D18-00-E-09	Ver: 9	Page: 9 / 21
---	-----------------	--------	--------------

5. Marking:

1.Type Designation	AS
2.Nominal Capacitance	Identified by 3-Figure Code. Ex. 100pF→"101" , 4700pF→"472"
3.Capacitance Tolerance	K:±10%,M:±20%
4.Company Name Code(Trade mark)	UK
5. Products ID	<p>Abbreviation ex.:</p> <p>Manufacture year: ← 5 C 6 1234 → Last 4 digits of lot no.</p> <p>5:2025 6:2026 7:2027 ·</p> <p>Individual specification code</p> <p>Manufacture month: 1:January 2:February · 9:September O:October N:November D:December</p> <p>Epoxy resin code: "·": Halogen and Pb free epoxy resin (For the last code "H" and "B" of SAP P/N)</p> <p>Manufactory: C:Pan overseas (Guangzhou)</p>
6.Approved Monogram:	
(1) VDE approval mark	 IEC 60384-14 Class code : X1 : 760V~ , Y1 : 500V~
(2) UL approval mark	
Marking ex.	
<p>Two sides marking (for SAP part number 10-11 digits ≤ "07" products)</p>	<p>One side marking (for SAP part number 10-11 digits ≥ "08" products)</p>
	
<p>* Marking by the laser.</p> <p>* "<u>C</u>" : Marked with code "·" stand for Halogen and Pb free epoxy resin.</p> <p>* "·" : Individual specification code, it is added under the lot no.</p>	

SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES	POE-D18-00-E-09	Ver: 9	Page: 10 / 21
--	-----------------	--------	---------------

6. Scope:

This specification applies to ceramic insulated capacitors disk type used in electronic equipment.

6.1 Applicable safety standard

This specification applies to the VDE, ENEC10,UL/CUL approved ceramic capacitors disc type for antenna coupling, line-by-pass and across-the-line. X1, Y1 capacitor based on IEC60384-14.

6.2 Safety standards approval and recognized no.

Safety Standard	Standard No.	Subclass	W.V.	Recognized No.
UL/cUL	UL 60384-14 (2nd ed.)	X1	760VAC	E146544 (FOWX2/FOWX8)
		Y1	500VAC	
VDE (ENEC)	DIN EN 60384-14 (VDE 0565-1-1):2024-10; EN 60384-14:2023-03 IEC 60384-14:2023	X1	760VAC	40039265
		Y1	500VAC	
ENEC (DEMKO)	EN 60384-14:2023	X1	760VAC	ENEC-05458
		Y1	500VAC	
CQC	GB/T6346.14-2023	X1	760VAC	CQC18001186964
		Y1	500VAC	



SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES	POE-D18-00-E-09	Ver: 9	Page: 11 / 21
--	-----------------	--------	---------------

7. Specification and test method:

7.1 Operating Temperature Range: -40 to +125°C

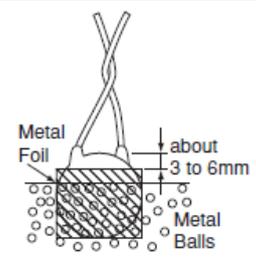
7.2 Test condition:

Test and measurement shall be made at the standard condition. (temperature 15~35°C, relative humidity 45~75% and atmospheric pressure 860~1060hpa). Unless otherwise specified herein.

If doubt occurred on the value of measurement, and measurement was requested by customer capacitors shall be measured at the reference condition. (temperature 20±2°C or 25 ± 2°C, relative humidity 60~70% and atmospheric pressure 860~1060hpa.)

7.3 Performance:

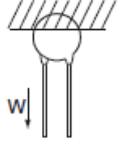
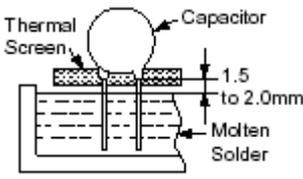
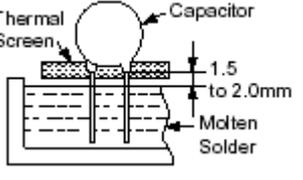
No	Items	Performance	Testing method										
7.3.1	Appearance And dimension	The appearance and dimension shall be as given in section 3.	Visual check.										
7.3.2	Marking	To be easily legible.	Visual check.										
7.3.3	Between terminals	No failure.	The capacitors shall not be damage when AC4000V (rms.) are applied between the lead wires for 60sec. (Charge/Discharge current ≤ 50mA.) Pre-treatment: Capacitor shall be stored at 125±2°C for 1hour.then placed at ※1room condition for 24±2hours before initial measurements.										
	Body Insulation	No failure.	First, the terminals of the capacitor should be connected together. Then, a metal foil should be closely wrapped around the body of the capacitor to the distance of about 3 to 6mm from each terminal. Then, the capacitor should be inserted into a container filled with metal balls of about 1mm diameter. Finally, AC4000V (r.m.s.)<50/60Hz> is applied for 60 s between the capacitor lead wires and metal balls. (Charge/Discharge current ≤ 50mA.) Pre-treatment: Capacitor shall be stored at 125±2°C for 1hour.then placed at ※1room condition for 24±2hours before initial measurements.										
7.3.4	Insulation Resistance	Between terminals 10000MΩ or more.	The insulation resistance shall be measured with DC500±50V within 60±5sec of charging.										
7.3.5	Capacitance	Within specified tolerance.	Y5P&Y5U: The capacitance shall be measured at 20±2°C with 1kHz±20% and 1.0Vrms.										
7.3.6	Dissipation Factor(D.F.)	Y5P、Y5U : D.F. ≤ 2.5%	Pre-treatment: Capacitor shall be stored at 125±2°C for 1hour.then placed at ※1room condition for 24±2hours before initial measurements.										
7.3.7	Temperature Characteristic	<table border="1"> <thead> <tr> <th>Char.</th> <th>Capacitance Change</th> </tr> </thead> <tbody> <tr> <td>Y5P</td> <td>Within ± 10%</td> </tr> <tr> <td>Y5U</td> <td>Within ± 20% ± 5%</td> </tr> </tbody> </table>	Char.	Capacitance Change	Y5P	Within ± 10%	Y5U	Within ± 20% ± 5%	The capacitance measurement shall be made at each step specified in Table .				
		Char.	Capacitance Change										
Y5P	Within ± 10%												
Y5U	Within ± 20% ± 5%												
<table border="1"> <thead> <tr> <th>Step</th> <th>1</th> <th>2</th> <th>3</th> <th>4</th> <th>5</th> </tr> </thead> <tbody> <tr> <td>Temp.(°C)</td> <td>+20±2</td> <td>-25±2</td> <td>+20±2</td> <td>+85±2</td> <td>+20±2</td> </tr> </tbody> </table>	Step	1	2	3	4	5	Temp.(°C)	+20±2	-25±2	+20±2	+85±2	+20±2	Pre-treatment: Capacitor shall be stored at 125±2°C for 1hour.then placed at ※1room condition for 24±2hours before initial measurements.
Step	1	2	3	4	5								
Temp.(°C)	+20±2	-25±2	+20±2	+85±2	+20±2								
7.3.8	Solderability of Leads	Lead wire should be soldered with uniform coating on the axial direction over 3/4 of the circumferential direction.	The lead wire of capacitor should be dipped into molten solder for 5 ± 0.5 sec. The depth of immersion is up to about 1.5 to 2.0 mm from the root of lead wires. Temp. of solder : Lead Free Solder (Sn-3Ag-0.5Cu) 245±5°C										



※ "room condition" temperature : 15~35°C, humidity : 45~75%,atmospheric pressure : 86~106kPa

※ "C" expresses nominal capacitance value (pF).

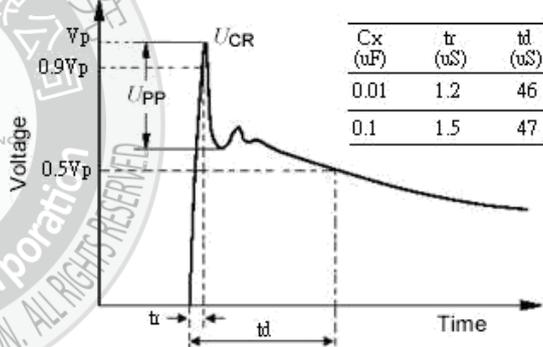
SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES	POE-D18-00-E-09	Ver: 9	Page: 12 / 21
--	-----------------	--------	---------------

No	Items	Performance	Testing method
7.3.9	Robustness of Terminations	Tensile Lead wire shall not cut off capacitor shall not be broken.	As shown in the figure at right, fix the body of the capacitor and apply a tensile weight gradually to each lead wire in the radial direction of the capacitor up to 10N and keep it for 10±1 sec. 
		Bending Lead wire shall not cut off. Capacitor shall not be broken.	With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical; a mass applying a force of 5N is then suspended from the end of the termination. The body of the specimen is then inclined, within a period of 2 to 3sec, through an angle of approximately 90° in the vertical plane and then returned to its initial position over the same period of time; this operation constitutes one bend. One bend immediately followed by a second bend in the opposite direction.
7.3.10	Soldering Effect (Non-Preheat)	Appearance No marked defect.	As shown in figure, the lead wires should be immersed in solder of 350 ± 10 °C or 260 ± 5 °C up to 1.5 to 2.0 mm from the root of terminal for 3.5 ± 0.5 sec (10 ± 1 sec. for 260 ± 5 °C). 
		I.R. 1000 MΩ min.	
		Dielectric Strength Per item 7.3. 3	
		Capacitance Y5P,Y5U : Within ±10 %	
7.3.11	Soldering Effect (On-Preheat)	Appearance No marked defect.	First the capacitor should be stored at 120+0/-5 °C for 60 +0/-5 sec. Then , as in figure , the lead wires should be immersed solder of 260+0/-5 °C up to 1.5 to 2.0 mm from the root of terminal for 7.5+0/-1 sec. 
		I.R. 1000 MΩ min.	
		Dielectric Strength Per item 7.3.3	
		Capacitance Y5P,Y5U : Within ±10 %	
			Pre-treatment: Capacitor shall be stored at 125±2°C for 1hour.then placed at *1room condition for 24±2hours before initial measurements. Post-treatment: Capacitor shall be stored for 1 to 2hours at *1room condition.
			Pre-treatment: Capacitor shall be stored at 125±2°C for 1hour.then placed at *1room condition for 24±2hours before initial measurements. Post-treatment: Capacitor shall be stored for 1 to 2hours at *1room condition.

※ "room condition" temperature : 15~35°C, humidity : 45~75%,atmospheric pressure : 86~106kPa

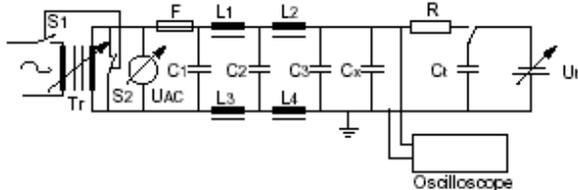
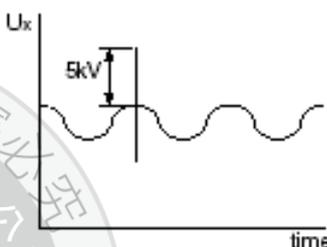
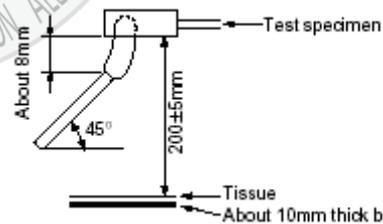
※ "C" expresses nominal capacitance value (pF).

SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES	POE-D18-00-E-09	Ver: 9	Page: 13 / 21
--	-----------------	--------	---------------

No	Items	Performance	Testing method									
7.3.12	Humidity (Under steady State)	Appearance	No marked defect.									
		Capacitance Change	Y5P : Within $\pm 10\%$ Y5U : Within $\pm 20\%$									
		D.F.	Y5P,Y5U : 5.0% max.									
		I.R.	Y5P&Y5U : 3000M Ω min.									
		Dielectric Strength	Per Item 7.3.3									
			Set the capacitor for 500 \pm 12hours at 40 \pm 2 $^{\circ}$ C in 90 to 95% relative humidity. Pre-treatment: Capacitor shall be stored at 125 \pm 2 $^{\circ}$ C for 1hour.then placed at *1 room condition for 24 \pm 2hours before initial measurements. Post-treatment: Capacitor shall be stored for 1 to 2hours at *1 room condition.									
7.3.13	Humidity Loading	Appearance	No marked defect.									
		Capacitance Change	Y5P : Within $\pm 10\%$ Y5U : Within $\pm 20\%$									
		D.F.	Y5P,Y5U : 5.0% max.									
		I.R.	Y5P&Y5U : 3000M Ω min.									
		Dielectric Strength	Per Item 7.3.3									
			Apply the rated voltage for 500 \pm 12 hours at 40 \pm 2 $^{\circ}$ C in 90 to 95% relative humidity. Pre-treatment: Capacitor shall be stored at 125 \pm 2 $^{\circ}$ C for 1hour.then placed at *1 room condition for 24 \pm 2hours before initial measurements. Post-treatment: Capacitor shall be stored for 1 to 2hours at *1 room condition.									
7.3.14	Life	Appearance	No marked defect.									
		Capacitance Change	Y5P&Y5U : Within $\pm 20\%$									
		I.R.	3000M Ω min.									
		Dielectric Strength	Per Item 7.3 3									
			<p>Impulse Voltage Each individual capacitor shall be subjected to 10kV impulses for three times. After the capacitors are applied to life test.</p>  <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>Cx (uF)</th> <th>tr (uS)</th> <th>td (uS)</th> </tr> </thead> <tbody> <tr> <td>0.01</td> <td>1.2</td> <td>46</td> </tr> <tr> <td>0.1</td> <td>1.5</td> <td>47</td> </tr> </tbody> </table> <p>The specimen capacitors are placed in a circulating air oven for a period of 1000 hours. The air in the oven is maintained at a temperature of 125\pm3$^{\circ}$C. Throughout the test, the capacitors are subjected to an AC850Vrms. alternating voltage of mains frequency.</p> <p>Pre-treatment: Capacitor shall be stored at 125\pm2$^{\circ}$C for 1hour.then placed at *1room condition for 24\pm2hours before initial measurements. Post-treatment: Capacitor shall be stored for 1 to 2hours at *1room condition.</p>	Cx (uF)	tr (uS)	td (uS)	0.01	1.2	46	0.1	1.5	47
Cx (uF)	tr (uS)	td (uS)										
0.01	1.2	46										
0.1	1.5	47										

※ "room condition" temperature : 15~35 $^{\circ}$ C, humidity : 45~75%,atmospheric pressure : 86~106kPa

※ "C" expresses nominal capacitance value (pF).

No	Items	Performance	Testing method																																	
7.3.15	Active Flammability	The cheesecloth shall not be on fire.	<p>The specimens shall be individually wrapped in at least one but more than two complete layers of cheesecloth. The specimens shall be subjected to 20 discharges. The interval between successive discharges shall be 5 sec. The UAC shall be maintained for 2 min after the last discharge.</p> <p>Fig. 6</p>  <p> C1,2 : $1\mu F \pm 10\%$ L1 to 4 : $1.5mH \pm 20\%$ C3 : $0.033\mu F \pm 5\%$ 10kV 16A Rod core choke Ct : $3\mu F \pm 5\%$ 10kV R : $100\Omega \pm 2\%$ Cx : Capacitor under test UAC : $UR \pm 5\%$ F : Fuse, Rated 10A UR : Rated Voltage Ut : Voltage applied to Ct </p> 																																	
7.3.16	Passive Flammability	The burning time shall not be exceeded the time 30 sec. The tissue paper shall not ignite.	<p>The capacitor under test shall be held in the position which best promotes burning. Each specimen shall only be exposed once to flame. Time of exposure to flame: 30sec.</p> <p>Length of flame : $12 \pm 1mm$ Gas burner : Length 35mm min. Inside Dia. : $0.5 \pm 0.1mm$ Outside Dia. : $0.9mm$ max. Gas : Butane gas Purity 95% min.</p> <p>Fig. 7</p> 																																	
7.3.17	Temperature Cycle	<table border="1" style="width:100%; border-collapse: collapse;"> <tr> <td colspan="2">Appearance</td> <td>No marked defect</td> </tr> <tr> <td>Char.</td> <td>Cap. Change</td> <td>DF</td> </tr> <tr> <td>Y5P</td> <td>$\leq \pm 10\%$</td> <td>$DF \leq 5.0\%$</td> </tr> <tr> <td>Y5U</td> <td>$\leq \pm 20\%$</td> <td>$DF \leq 7.5\%$</td> </tr> <tr> <td colspan="2">I.R.</td> <td>3000MΩ min.</td> </tr> <tr> <td colspan="2">Dielectric strength</td> <td>Per Item 7.3.3</td> </tr> </table>	Appearance		No marked defect	Char.	Cap. Change	DF	Y5P	$\leq \pm 10\%$	$DF \leq 5.0\%$	Y5U	$\leq \pm 20\%$	$DF \leq 7.5\%$	I.R.		3000M Ω min.	Dielectric strength		Per Item 7.3.3	<p>The capacitor should be subjected to 5 temperature cycles, <Temperature Cycle time: 5cycles></p> <table border="1" style="width:100%; border-collapse: collapse; text-align: center;"> <thead> <tr> <th>Step</th> <th>Temperature(°C)</th> <th>Time(min)</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>-40+0/-3</td> <td>30</td> </tr> <tr> <td>2</td> <td>Room temp.</td> <td>3</td> </tr> <tr> <td>3</td> <td>125+3/-0</td> <td>30</td> </tr> <tr> <td>4</td> <td>Room temp.</td> <td>3</td> </tr> </tbody> </table> <p>Pre-treatment: Capacitor shall be stored at $125 \pm 2^\circ C$ for 1hour, then placed at ¹room condition for 24 ± 2hours.</p> <p>Post-treatment: Capacitor shall be stored for 1 to 2hours at ^{*1}room condition.</p>	Step	Temperature(°C)	Time(min)	1	-40+0/-3	30	2	Room temp.	3	3	125+3/-0	30	4	Room temp.	3
Appearance		No marked defect																																		
Char.	Cap. Change	DF																																		
Y5P	$\leq \pm 10\%$	$DF \leq 5.0\%$																																		
Y5U	$\leq \pm 20\%$	$DF \leq 7.5\%$																																		
I.R.		3000M Ω min.																																		
Dielectric strength		Per Item 7.3.3																																		
Step	Temperature(°C)	Time(min)																																		
1	-40+0/-3	30																																		
2	Room temp.	3																																		
3	125+3/-0	30																																		
4	Room temp.	3																																		

※ "room condition" temperature : $15 \sim 35^\circ C$, humidity : 45~75%, atmospheric pressure : 86~106kPa

※ "C" expresses nominal capacitance value (pF).

SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES	POE-D18-00-E-09	Ver: 9	Page: 15 / 21
--	-----------------	--------	---------------

8.Packing Baggage :

8.1 Packing size:

Type	Box	Carton	
Bulk			
Ammo taping			

8.2 Packing quantity:

Packing type	The code of 14th to 15th in SAP P/N	MPQ (Kpcs/Box)	Remark
Taping	AM (The size code ≤ 11)	1	F=10mm (Code -17th"A" or "0")
	AM (The size code ≥ 12)	0.5	
	AM(Code -17th"C")	0.5	F=12.5mm
	AS	1	
	AT	0.5	

Packing type	Lead length	Size code of 10th to 11th in SAP P/N	MPQ (Kpcs/Bag)	Kpcs/Box
Bulk	Long lead (L ≥ 20 mm)	07~11	0.5	1.5
		12-14	0.5	1
		16	0.2	1
		17-19	0.1	0.5
	Short lead (L < 20mm)	07~11	0.5	2
		12-14	0.5	1.5
		16	0.2	1
		17-19	0.1	0.5

8.3 Label samples:

CUST P/N: 2576721

NEW POE P/N: YU0AS472M140DAMD0B QTY: 500 PCS

POE P/N: YU0AS472M140DAMD0B

CAP: 4700pF TOL: + -20%

VOLT: X1: 760VAC, Y1: 500VAC T.C: Y5U

LOT NO.: 741GA07307 DATE: 2018.03.30

PAN OVERSEAS(GUANGZHOU) ELECTRONIC CO.,LTD.

R001

UL60384-14
IEC60384-14
40/125/21/C

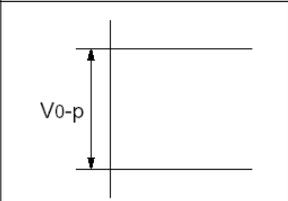
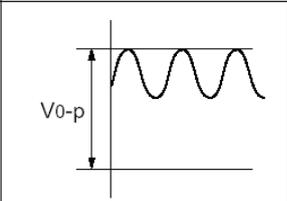
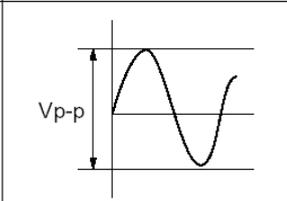
9. Notices:

9.1 Caution (Rating):

(1). Operating Voltage

Be sure to maintain the V_{p-p} value of the applied voltage or the V_{0-p} which contains DC bias within the rated voltage range.

When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing this irregular voltage.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage
Positional measurement			

(2). Operating Temperature and Self-generated Heat

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself.

(3). Test condition for withstanding Voltage

I. Test Equipment

Test equipment for AC withstanding voltage shall be used with the performance of the wave similar to 50/60 Hz sine waves.

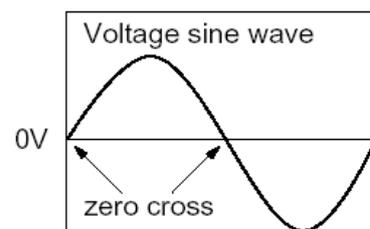
If the distorted sine wave or over load exceeding the specified voltage value is applied, the defective may be caused.

II. Voltage Applied Method

When the withstanding voltage is applied, capacitor's lead or terminal shall be firmly connected to the output of the withstanding voltage test equipment, and then the voltage shall be raised from near zero to the test voltage.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, test voltage should be applied with the *zero cross. At the end of the test time, the test voltage shall be reduced to near zero, and then capacitor's lead or terminal shall be taken off the output of the withstanding voltage test equipment.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, the surge voltage may arise, and therefore, the defective may be caused.



ZERO CROSS is the point where voltage sine wave pass 0V.- See the right figure.

III. Applied voltage

The voltages of Table shall be applied between the respective measuring points of 1 min for qualification approval and periodic testing and for a period of not less than 1 s for lot-by-lot quality conformance testing, a voltage proof test such as Test C shall be carried out only for qualification approval tests and periodic tests;

Attention is drawn to the fact that repetition of the voltage proof test by the user may damage the capacitor. If repetition of the voltage proof test is made by the user, the applied voltage should not be greater than 66 % of the test voltage specified in Table .

Table –Voltage proof

Class	Range of rated voltages	Test A	Test B or Test C
X1	$\leq 1\ 000\ \text{V}$	$4,3\ UR\ (\text{d.c.})\ c$	$2\ UR + 1\ 500\ \text{V (a.c.)}$ with a minimum of $2\ 000\ \text{V (a.c.)}$ a
Y1	$\leq 500\ \text{V}$	$4\ 000\ \text{V (a.c.)}$	$4\ 000\ \text{V (a.c.)}$

a For Delta and T-connected capacitor units according to Figures 5b and 5c, the test voltage for terminals to case shall be the appropriate test voltage for the Y-capacitors.
b The UR in this d.c. test is the rated a.c.voltage value.

Note:

Test A – Between terminations

Test B – Internal insulation

Test C – External insulation (applicable only to insulated capacitors in nonmetallic case or in insulated metal case)

Figure 5b – Delta by-pass capacitor (in metallic housing)

Figure 5c – Example of a T-connected by-pass capacitor (in non-metallic housing)

Figure 5b

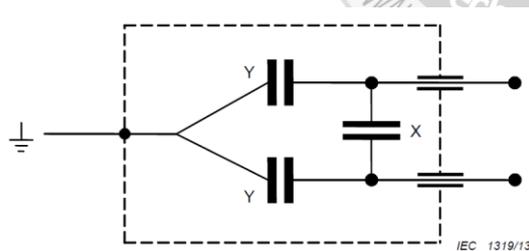
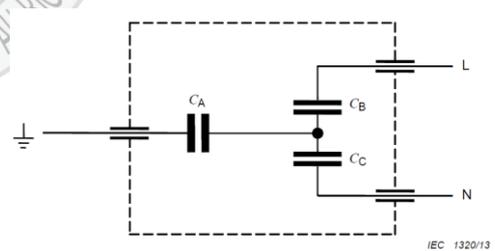


Figure 5c



***For capacitors with non-metallic housings, the earth connection is brought out as a separate termination as is shown in Figure 5c.**

(4). Fail-Safe

When capacitor would be broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.

9.2 Caution (Storage and operating condition):

Operating and storage environment

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed -10 to 40 degrees centigrade and 15 to 85 % for 6 months maximum and use within the period after receiving the capacitors.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."

9.3 Caution (Soldering and Mounting):

9.3.1 Vibration and impact:

Do not expose a capacitor or its leads to excessive shock or vibration during use.

9.3.2 Soldering:

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron-tip: 400 degrees C. max.

Soldering iron wattage: 50W max.

Soldering time: 3.5 sec. max.

9.3.3 Cleaning (ultrasonic cleaning):

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less.

Rinsing time: 5 min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."

SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES	POE-D18-00-E-09	Ver: 9	Page: 19 / 21
--	-----------------	--------	---------------

9.3.4 List of substances that affect the insulation strength of coating :

Epoxy resin solvent

Category	Model		
Ketone	Acetone	Butanone	Cyclohexanone
Esters	Ethyl acetate	Dibutyl phthalate	
Chlorinated hydrocarbons	Dichloromethane		

Epoxy resin thinner

Category		Model	
Reactive diluentactivated thinner	Simple function group	HK-66 (Alkyl glycidyl ether)	
		501 (Butyl glycidyl ether)	
		690 (Phenyl Glycidyl Ether)	
		AGE (C12-14Aliphatic Polyalcohol Glycidyl Ether)	
		692 (Benzyl Glycidyl Ether)	
	Two functional groups	D-678 (Neopentyl glycol diglycidyl ether)	
		622 (1,4-Butanediol diglycidyl ether)	
		669 (Ethylene glycol diglycidyl ether)	
		X-632 (Polypropylene glycol diglycidyl ether)	
		X-652 (1,6-Hexadiol diglycidyl ether)	
Non-activated thinner	D-691Epoxypropane o-methylphenyl ether		
	Anhydrous ethanol	Toluene	
	Ethyl acetate	Dimethylbenzene	
	Dimethyl formamide	Butyl acetate	
	Acetone	Styrene	
	Polyol	Benzyl alcohol	

Note: The above substances should not contact the coating of the product body, otherwise it will affect the insulation strength of the product

9.4 Caution (Handling):

Vibration and impact

Do not expose a capacitor or its leads to excessive shock or vibration during use.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."

10. Soldering Recommendation:

10.1 Wave Soldering Profile:

- Temperature conditions of the flow is recommended as shown in the chart
- Must implement the pre-heat
- Maximum peak flow temperature is recommended 265°C
- Time “T” implement in the chart recommended within 20 sec. if temperature exceed 200°C
- Take care with the flow solder not to touch the capacitor body directly at mounting

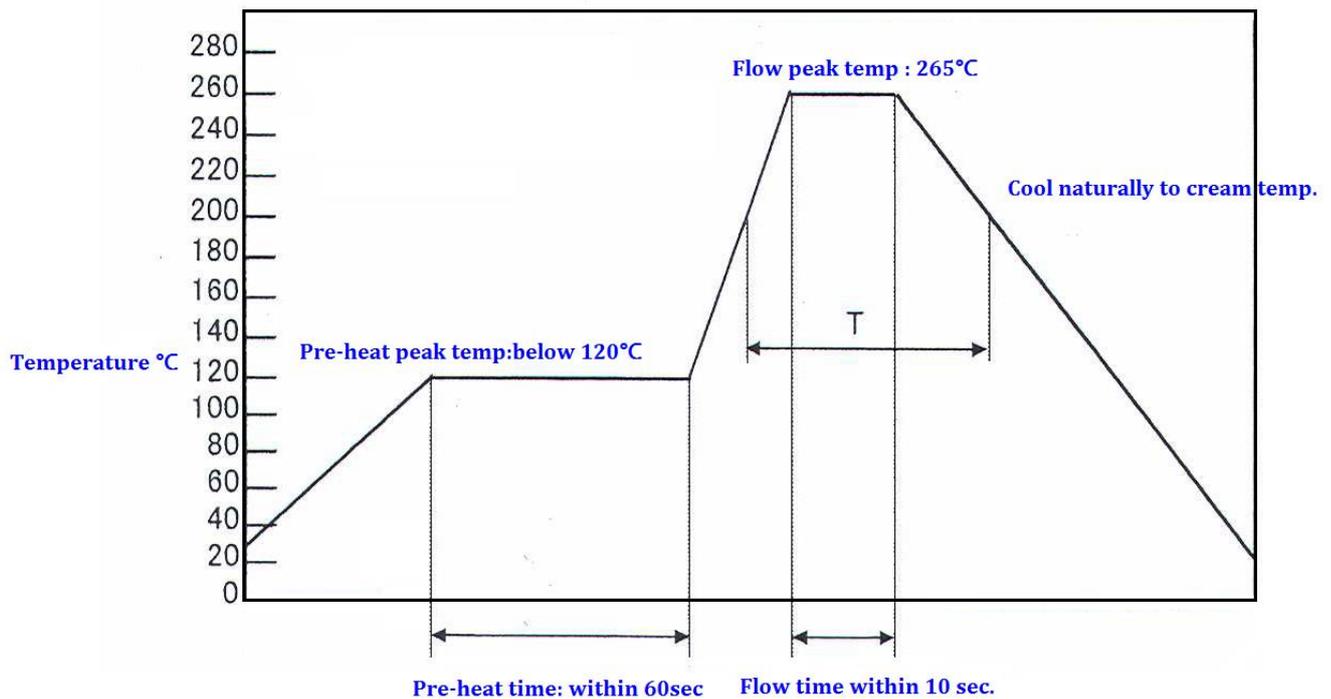


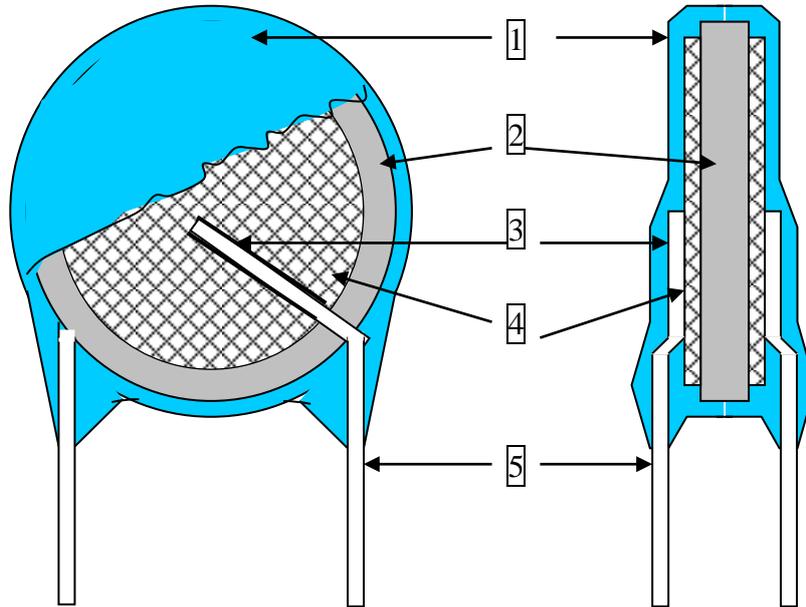
Chart to show flow recommended temp

10.2 Recommended Reworking Conditions with Soldering Iron :

- Temperature of iron-tip: 400 degrees C. max.
- Soldering iron wattage: 50W max.
- Soldering time: 3.5 sec. max.
- Distance from coating body: 2 mm (min.)

10.3 Reflow-Soldering : Lead Ceramic Cap. should not be soldered by reflow-soldering.

11. Drawing of internal structure and material list:



Remarks :

No.	Material	Description
1	Insulation Coating	Epoxy resins (Conforming to UL94V-0 standard) Type No.: confidential
2	Dielectric Element	Ceramic
3	Solder	Tin-Silver alloy
4	Electrodes	Silver [Ag]
5	Leads wire	Solder coated CP wire (Solder : Tin-Silver alloy)